

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"20040017365".PN.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 09:15
S2	3551	(buffer NEAR1 (tft OR transistor))	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 09:33
S3	157	S2 AND "345"/\$.OCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 09:36
S4	698	(shift ADJ1 register) NEAR1 (tft OR transistor)	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:28
S5	103	S4 AND "345"/\$.OCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:29
S6	279	("d/a converter" OR (digital ADJ1 analog ADJ1 converter)) NEAR2 (tft OR transistor)	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:32
S7	44	S6 AND "345"/\$.OCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:32
S8	6	(shift\$5 ADJ0 register) WITH (digital ADJ1 analog ADJ1 converter) WITH buffer WITH (sampling ADJ1 circuit)	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:38
S9	1	S8 AND "345"/\$.OCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:39

S10	6	(shift\$5 ADJ0 register) SAME (digital ADJ1 analog ADJ1 converter) SAME buffer SAME (sampling ADJ1 circuit)	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:39
S11	1	S10 AND "345"\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:39
S12	760	(shift\$5 ADJ1 register) AND (buffer) AND (samp\$5 ADJ1 circuit \$3) AND ((digital ADJ1 analog ADJ1 converter) OR "d/a converter")	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:44
S13	150	S12 AND "345"\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:44
S14	20	S12 AND hitachi.AS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:47
S15	1	"6949452".PN.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/18 10:47
S16	1	"6483495".PN.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:18
S17	783	(shift ADJ1 register) SAME ("d/a converter" OR (digital ADJ1 analog ADJ1 converter)) SAME buffer	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:20
S18	331	S17 AND "345"\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:20
S19	3	S18 AND (plural\$5 ADJ1 ic)	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:20
S20	1956	(shift ADJ1 register) SAME (converter) SAME buffer	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:25

S21	560	S20 AND "345"/\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:25
S22	169	S18 AND (ic OR (integrated ADJ1 circuit))	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:25
S23	783	(shift ADJ1 register) SAME ("d/a converter" OR (digital ADJ1 analog ADJ1 converter)) SAME buffer	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:41
S24	331	S23 AND "345"/\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:41
S25	169	S24 AND (ic OR (integrated ADJ1 circuit))	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:41
S26	0	S25 AND (semiconductor).AN.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:41
S27	35	S25 AND (semiconductor).AS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 08:41
S28	783	(shift ADJ1 register) SAME ("d/a converter" OR (digital ADJ1 analog ADJ1 converter)) SAME buffer	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:25
S29	331	S28 AND "345"/\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:25
S30	169	S29 AND (ic OR (integrated ADJ1 circuit))	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:25
S31	355	(sampling ADJ1 circuit) AND (ic OR (integrated ADJ1 circuit)) AND "345"/\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:33

S32	214	(shift ADJ1 register) SAME ("d/a converter" OR (digital ADJ1 analog ADJ1 converter)) SAME buffer SAME (sampling ADJ1 circuit)	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:38
S33	23	S32 AND "345"/\$.CCLS.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:39
S34	14	S32 AND "345"/\$.CCLS. AND ic	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:44
S35	1	"6483495".PN.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 09:51
S36	1	"20040017365".PN.	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/04/21 10:03

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